

WHAT IS CLAIMED IS:

1. A power amplification circuit comprising a power amplifier and a negative feedback circuit connected between a grounding terminal of the power amplifier and ground, wherein

impedance of the negative feedback circuit depends on a signal voltage occurring across the negative feedback circuit.

2. The power amplification circuit according to Claim 1, wherein

the impedance of the negative feedback circuit decreases as the signal voltage occurring across the negative feedback circuit increases.

3. The power amplification circuit according to Claim 1, wherein

the negative feedback circuit is a series connection circuit in which a diode and a capacitance device are connected in series or a series connection circuit in which a diode, a capacitance device and a feedback resistor are connected in series,

a connecting point between the diode and the capacitance device is grounded via a grounding resistor, and

a bias power supply for the power amplifier is connected to one end of the series connection circuit so that the diode is biased.

4. The power amplification circuit according to Claim 3, wherein

the diode is constituted of a junction between two terminals out of base, collector and emitter of a bipolar transistor.

5. The power amplification circuit according to Claim 4, wherein

the power amplifier is constituted of a bipolar transistor,

the bipolar transistor constituting the diode and the bipolar transistor used for the power amplifier are generally equal to one another in bias-current temperature characteristics.

6. The power amplification circuit according to Claim 3, wherein

the diode is constituted of a junction between two terminals out of gate, drain and source of a field effect transistor.

7. A communication device in which the power amplification circuit as defined in Claim 1 is used for a transmitting section.